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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/812,526	03/29/2004	Kong Weng Lee	70030845-1	3297	
57299 7.	590 01/19/2006		EXAMINER		
AVAGO TECHNOLOGIES, INC.			MAKIYA, DAVID J		
P.O. BOX 1920 DENVER, CO			ART UNIT	PAPER NUMBER	
DENVER, CO	00201 1920		2875	 	
		,	DATE MAILED: 01/19/2006	6	

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary		Applic	ation No.	Applicant(s)				
		10/812	:,526	LEE ET AL.				
		Exami	ner	Art Unit				
			J. Makiya	2875				
Period fo	The MAILING DATE of this communica or Reply	ition appears on	the cover sheet with	the correspondence a	ddress			
WHIC - Exter after - If NO - Failu Any (CRTENED STATUTORY PERIOD FOR CHEVER IS LONGER, FROM THE MAI asions of time may be available under the provisions of SIX (6) MONTHS from the mailing date of this community period for reply is specified above, the maximum statute to reply within the set or extended period for reply will reply received by the Office later than three months after the patent term adjustment. See 37 CFR 1.704(b).	LING DATE OF 37 CFR 1.136(a). In no cation. ory period will apply an , by statute, cause the	THIS COMMUNICATION OF EVENT, however, may a reput will expire SIX (6) MONTH application to become ABA	ATION. Only be timely filed HS from the mailing date of this of NDONED (35 U.S.C. § 133).				
Status								
1)⊠	Responsive to communication(s) filed	on 15 Decembe	r 2005.					
	This action is FINAL . 2b) This action is non-final.							
•	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is							
,—	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Dispositi	on of Claims							
4)⊠	4)⊠ Claim(s) <u>1-5,8-12 and 15-21</u> is/are pending in the application.							
	4a) Of the above claim(s) is/are withdrawn from consideration.							
	Claim(s) is/are allowed.							
· · · · · · · · · · · · · · · · · · ·	☑ Claim(s) <u>1-5,8-12 and 15-21</u> is/are rejected.							
	•							
8)	Claim(s) are subject to restriction and/or election requirement.							
Applicati	on Papers							
9)□	The specification is objected to by the E	- - - - - - -						
	•		epted or b) abie	cted to by the Examine	·r			
10)⊠ The drawing(s) filed on <u>29 March 2004</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
	Replacement drawing sheet(s) including th	= :	·	· ·	FR 1.121(d).			
11)[The oath or declaration is objected to b			•				
Priority u	ınder 35 U.S.C. § 119							
12)	Acknowledgment is made of a claim for	foreign priority	under 35 U.S.C. & 1	119(a)-(d) or (f)				
	12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of:							
- /.	1. Certified copies of the priority documents have been received.							
	2. Certified copies of the priority documents have been received in Application No							
	3. Copies of the certified copies of the priority documents have been received in this National Stage							
	application from the Internationa	•						
* 5	See the attached detailed Office action f	•		eceived.				
Attachmen			□ · · · -	(0.00				
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)			4) Interview Sur Paper No(s)/	mmary (PTO-413) 'Mail Date				
3) 🔲 Inform	nation Disclosure Statement(s) (PTO-1449 or PT		5) Notice of Info	ormal Patent Application (PT	O-152)			
Pape	r No(s)/Mail Date		6) Other:					

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1, 8, and 15-21 are rejected under 35 U.S.C. 102(e) as being anticipated by Collins, III et al. (US 2005/0184387).

With respect to claim 1, Collins, III et al. teaches a light emitting diode package comprising a ceramic substrate for mounting a light emitting diode 60, the substrate defining a cavity with a ceramic sidewall, wherein the cavity is shaped to focus light in a predetermined direction, and a metallic coating 64 on a portion of the ceramic substrate for reflecting light in a predetermined direction. The examiner is interpreting the reference such that 64 and 40 define the "ceramic substrate" defining a light-focusing cavity as claimed (Figure 5). Collins, III et al. further teaches the substrate portion to define a ceramic sidewall that also operates as the reflective wall covered in metal (Paragraph 34).

With respect to claim 8, Collins, III et al. teaches a method for manufacture of a light emitting diode package comprising forming a ceramic substrate for mounting a light emitting diode 60, the substrate defining a cavity with a ceramic sidewall, and the cavity having a bottom and a top (Figure 5), wherein the cavity is shaped to focus light in a predetermined direction, coating a portion 64 of the ceramic cavity with a light reflective material, positioning a light

emitting diode on the substrate, and depositing an optically transparent material in the cavity to protect the light emitting diode. The examiner is interpreting the reference such that 64 and 40 define the "ceramic substrate" defining a light-focusing cavity and a positioned light emitting diode as claimed (Figure 5). Collins, III et al. further teaches the substrate portion to define a ceramic sidewall that also operates as the reflective wall covered in a light reflective material (Paragraph 34).

With respect to claims 15-17, Collins, III et al. teaches the method wherein positioning the light emitting diode comprises determining a location between the bottom and the top of the cavity to locate the light emitting diode. It is an inherent characteristic of a light-emitting device to have a viewing angle. Based on the structure of the reference light emitting diode package, positioning the light emitting diode within the cavity will result in light emitting only within an angle created by the cavity. It is therefore inherent in the structure of the device that positioning the light emitting diode within the cavity will achieve a predetermined viewing angle of the light emitting diode while moving the light emitting diode closer to the bottom of the cavity will reduce the viewing angle and moving it closer to the top of the cavity will increase the viewing angle (Refer to Paragraph 34).

With respect to claim 18, Collins, III et al. teaches the method wherein depositing the optically transparent material in the cavity to protect to light emitting diode comprises forming a domed layer 66 of the optically transparent material over the light emitting diode (Paragraph 34).

With respect to claim 19, Collins, III et al. teaches the method as described above, but fails to teach the optically transparent material forming a concaved layer. Abe teaches the method of depositing an optically transparent material to protect a light emitting diode comprises

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would have been obvious to one of ordinary skill in the art at the time of the invention to modify the Collins, III et al. method with the teachings of Abe because having a concaved layer over the LED provides the ability to focus the emitted light in a more concentrated area. In response to applicant's argument that there is no reason to combine because Abe does not suggest the use of a ceramic sidewall, the test for obviousness is not whether the features of a secondary reference may be bodily incorporated into the structure of the primary reference; nor is it that the claimed invention must be expressly suggested in any one or all of the references. Rather, the test is what

forming a concaved layer of the optically transparent material over the light emitting diode. It

the combined teachings of the references would have suggested to those of ordinary skill in the art. See *In re Keller*, 642 F.2d 413, 208 USPQ 871 (CCPA 1981). In this case, the primary

reference teaches all of the limitations of the independent claim and the secondary reference

teaches the missing limitation of the secondary claim. In addition, the examiner provides

motivation for the combination of the references, therefore providing a case of prima facie

evidence.

With respect to claims 20 and 21, Collins, III et al. teaches the ceramic sidewall is vertical (Figure 5).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all

obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 2-5 and 9-12 are rejected under 35 U.S.C. 103(a) as being unpatentable over Collins, III et al. in view of Ishinaga (US Patent 6,355,946).

With respect to claims 2-5, Collins, III et al. teaches the light emitting diode package as described in claim 1, but fails to explicitly state the shape of the cavity. Ishinaga teaches the use of rectangular (Figure 8), trapezoidal (Figure 7), oval (Figure 2), and circular (Figure 12) shaped cavities. It would have been obvious to one of ordinary skill in the art at the time of the invention to modify the Collins, III et al. package with the teachings of Ishinaga because different shapes would provide different illumination patterns and would increase the aesthetic appearance based on the application of the device.

With respect to claims 9-12, Collins, III et al. teaches the method for manufacture of a light emitting diode package as described in claim 8, but fails to explicitly state the shape of the cavity. Ishinaga teaches the use of rectangular (Figure 8), trapezoidal (Figure 7), oval (Figure 2), and circular (Figure 12) shaped cavities. It would have been obvious to one of ordinary skill in the art at the time of the invention to modify the Collins, III et al. method with the teachings of Ishinaga because different shapes would provide different illumination patterns and would increase the aesthetic appearance based on the application of the device.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO

MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David J. Makiya whose telephone number is (571) 272-2273.

The examiner can normally be reached on Monday-Friday 7:30am - 4:00pm (ET).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Renee Luebke can be reached on (571) 272-2009. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DJM 01/09/2006

RENEE LUEBKE